

Substitute for form 1449A/PTO				Complete if Known	
				Application Number	09/808,067
				Filing Date	March 14, 2001
				First Named Inventor	Abbott et al.
				Group Art Unit	2505
				Examiner Name	Jarret, Scott L.
Sheet	1	of	2	Attorney Docket Number	YOR920000681US1

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant/Inventor of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
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		T ²			
		IBM Watson Implosion Technology Release 6.0, User's Guide and Reference, April 17, 2006-Draft Document, Second Edition (March 2000), 410 pages total			
		Gupta, et al., "An Optimization Approach for A Reverse Logistics Supply Chain," Proceedings of the International Group Technology and Cellular Manufacturing Conference, 2000, pp. 1-6			
		Veerakamolmal, P., "DESIGN AND ANALYSIS OF DISASSEMBLY AND REMANUFACTURING SYSTEMS IN THE ELECTRONICS INDUSTRY," UMI Microform 9937438, Copyright 1999, June 1999, pp. 1-118			
		Gupta, et al., "DISASSEMBLY OF PRODUCTS," NIST Systems Integration for Manufacturing Application Program, Grant No. 60NANB5D0112, Final Report, December 1996, pp. 1-195			
		Gupta, et al., "PROFITABILITY MEASURE FOR PRODUCT DISASSEMBLY AND RECYCLING," Proceedings of the 1998 Production and Operations Management Society Conference, June 1998, pp. 139-147			
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		Gupta, et al., "Definitions for (Article #1351) Environmental Issues: Reuse and Recycling in manufacturing Systems," Encyclopedia of Production and Manufacturing Management, August 10, 1998, pp. 1-5			
		Veerakamolmal, et al, "Design of an Integrated Component Recovery System," Proceedings of the 1998 IEEE International Symposium on Electronics and the Environment, May 4-6, pp. 264-269			
		Veerakamolmal, et al, "High-mix/Low-volume Batch of Electronic Equipment Disassembly," Computers and Industrial Engineering, Col. 35(1-2), pp. 65-68			

Examiner Signature	Date Considered
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<p>Substitute for form 1449A/PTO</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p><i>(use as many sheets as necessary)</i></p>				<p>Complete if Known</p>	
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		Veerakamolmal, et al., "Optimal Analysis of Lot-Size Balancing for Multiproducts Selective Disassembly," International Journal of Flexible Automation and Integrated Manufacturing, Vol. 6(3&4), 1998, pp. 245-269	
		Veerakamolmal, et al., "Planning Components Recovery from Multiple Products," Proceedings of the 1998 Northeast Decision Sciences Institute Conference, March 25-27, pp. 270-272	
		Veerakamolmal, et al., "A Combinatorial Cost-Benefit Analysis Methodology for Designing Modular Electronic Products for the Environment," Proceedings of the 1999 IEEE International Symposium on Electronics and the Environment, May 11-13, 6 pages	
		Veerakamolmal, et al., "Automating Multiple Products Disassembly Process Planning with Case-Based Reasoning," Proceedings of the Second International Conference on Operations and Quantitative Management, January 3-6, pp. 24-33	
		Veerakamolmal, et al., "DESIGNING ELECTRONIC PRODUCTS FOR DISASSEMBLY USING CONT/BENEFIT ANALYSIS," Proceedings of the 1999 Annual Meeting of the Northeast Decision Sciences Institute, March 24-26, pp. 189-191	
		Veerakamolmal, et al., "Reusable-Component Requirements Planning for the Integrated Remanufacturing System," Proceedings of the 25 th International Conference on Computers and Industrial Engineering, March 29-April 1, pp. 58-61	
		Veerakamolmal, et al., "DISASSEMBLY PROCESS PLANNING," Engineering Design and Automation, March 18, 1997, pp. 1-26	
		Veerakamolmal, et al., "ANALYSIS OF DESIGN EFFICIENCY FOR THE DISASSEMBLY OF MODULAR ELECTRONIC PRODUCTS," Journal of Electronics Manufacturing, Vol. 9, No. 1, March 1999, pp. 79-95	
		Veerakamolmal, et al., "Disassembly," Industrial Engineering Encyclopedia, 6 pages	
		Veerakamolmal, et al., "Disassembly Process Planning," International Conference on Engineering Design and Automation, March 18-21, 4 pages	

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